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FIG. 1

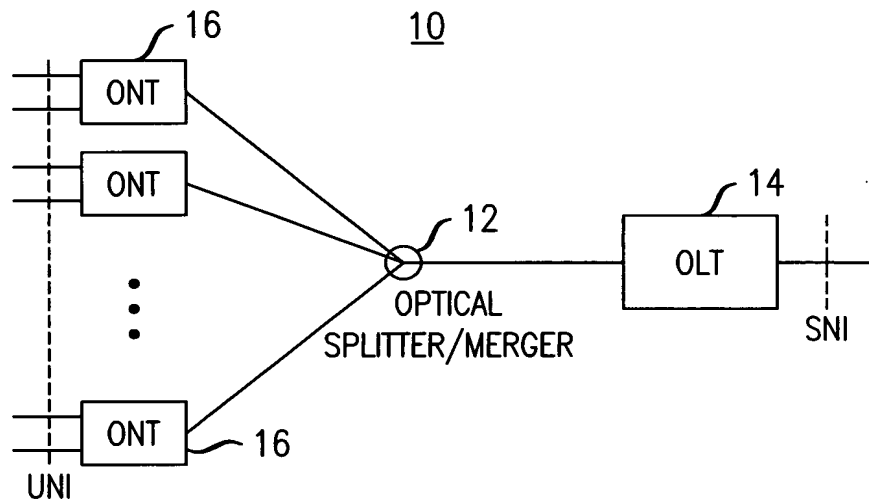
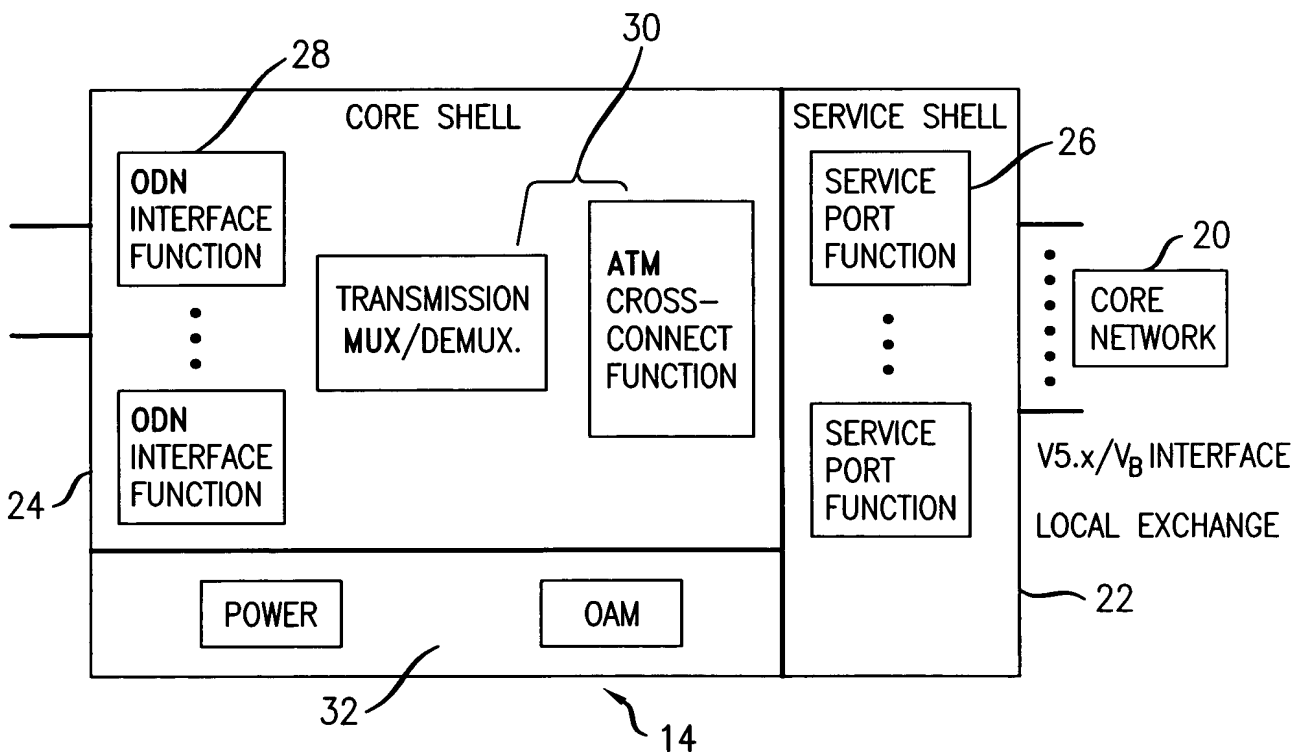
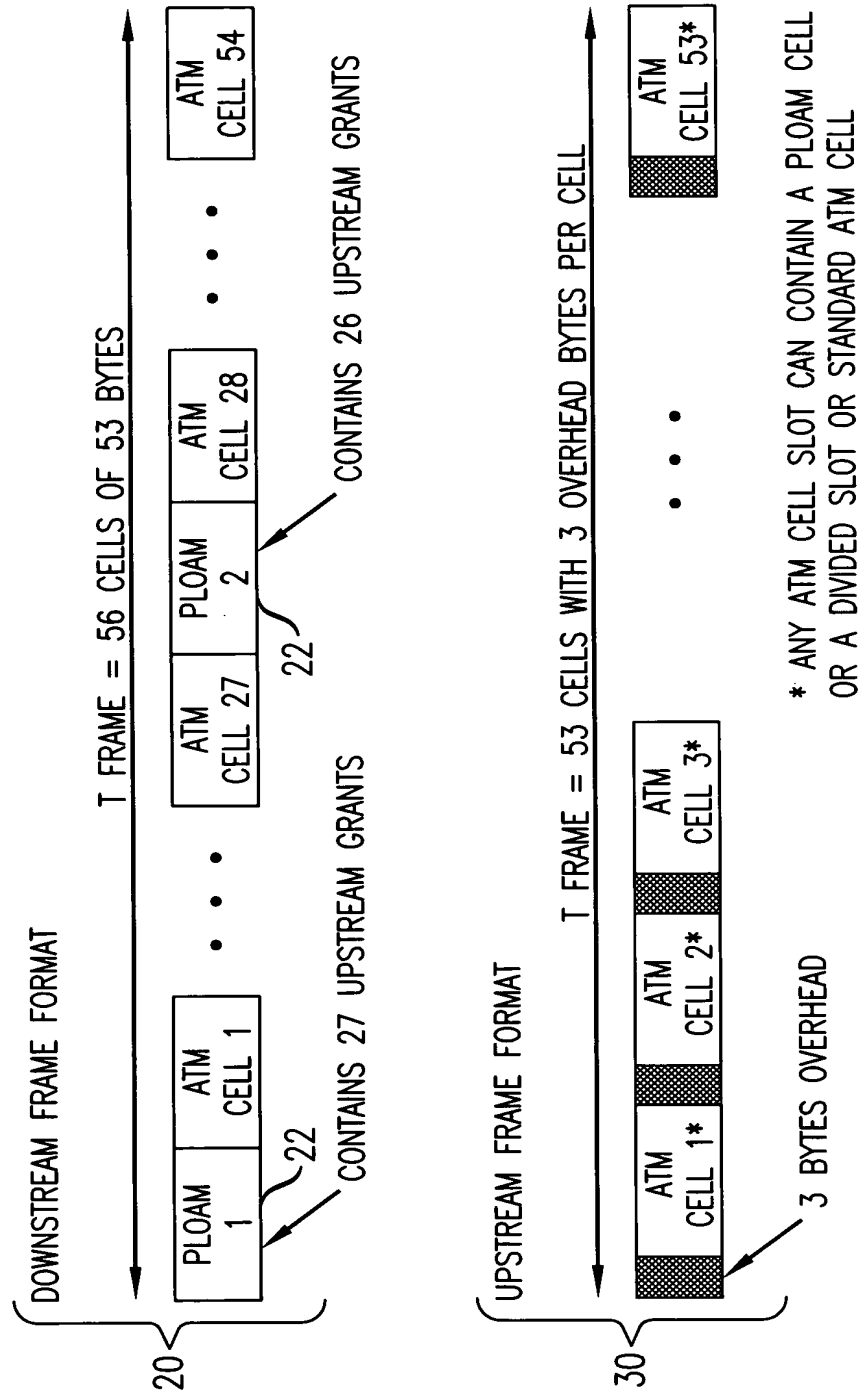


FIG. 2



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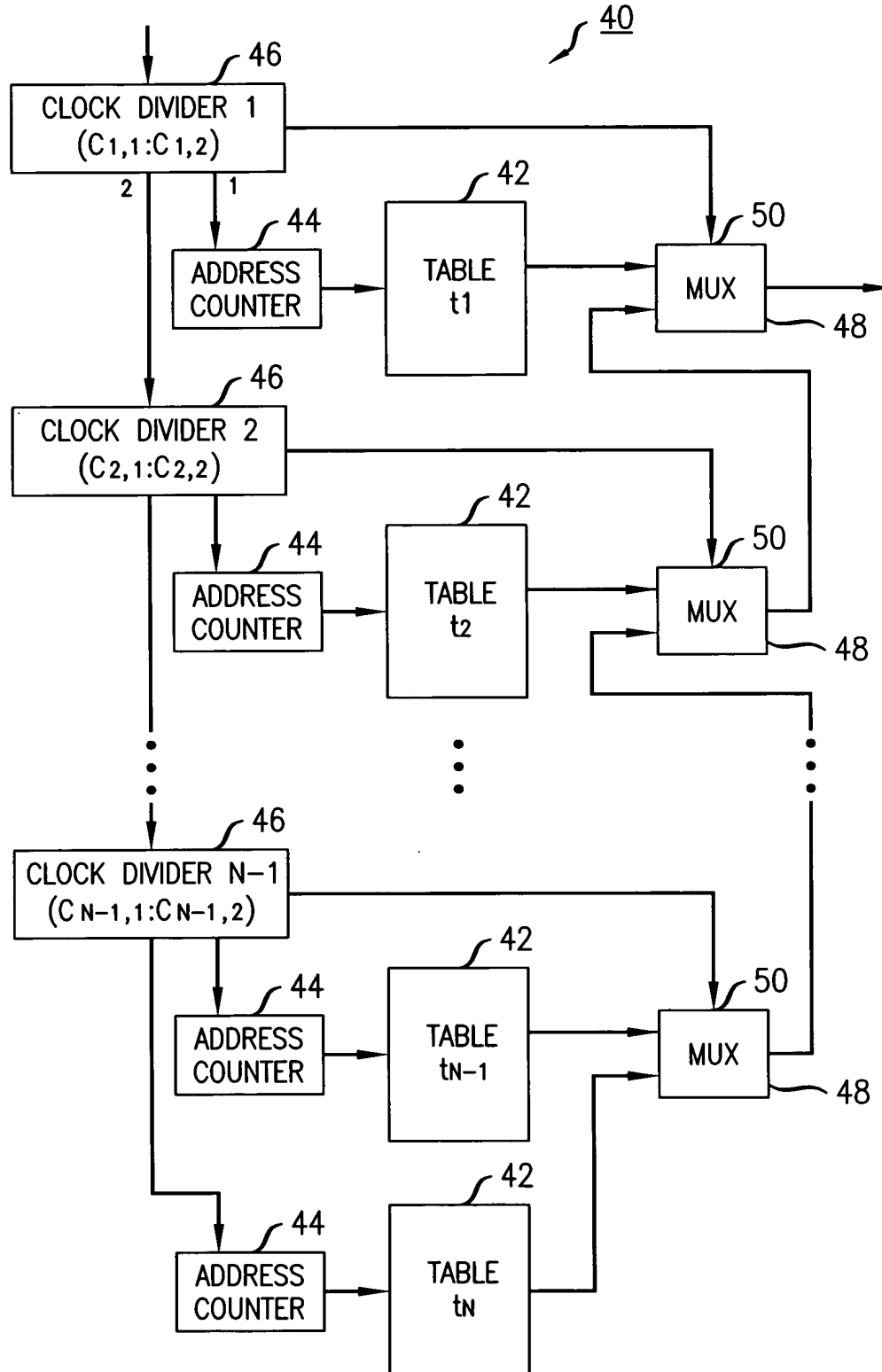
FIG. 3



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FIG. 4

RECURSIVE IMPLEMENTATION OF GRANT GENERATOR USING MULTI-TABLES



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FIG. 5A

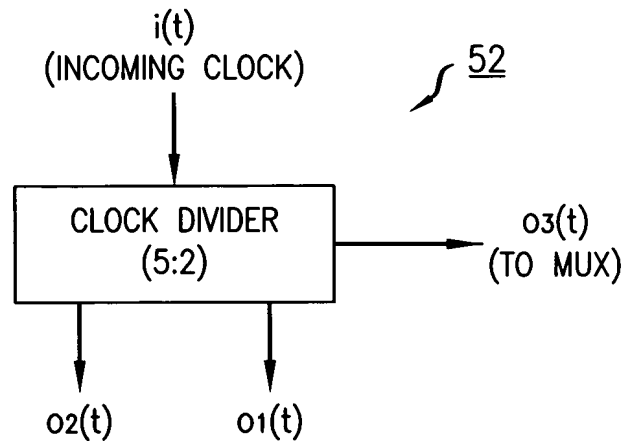
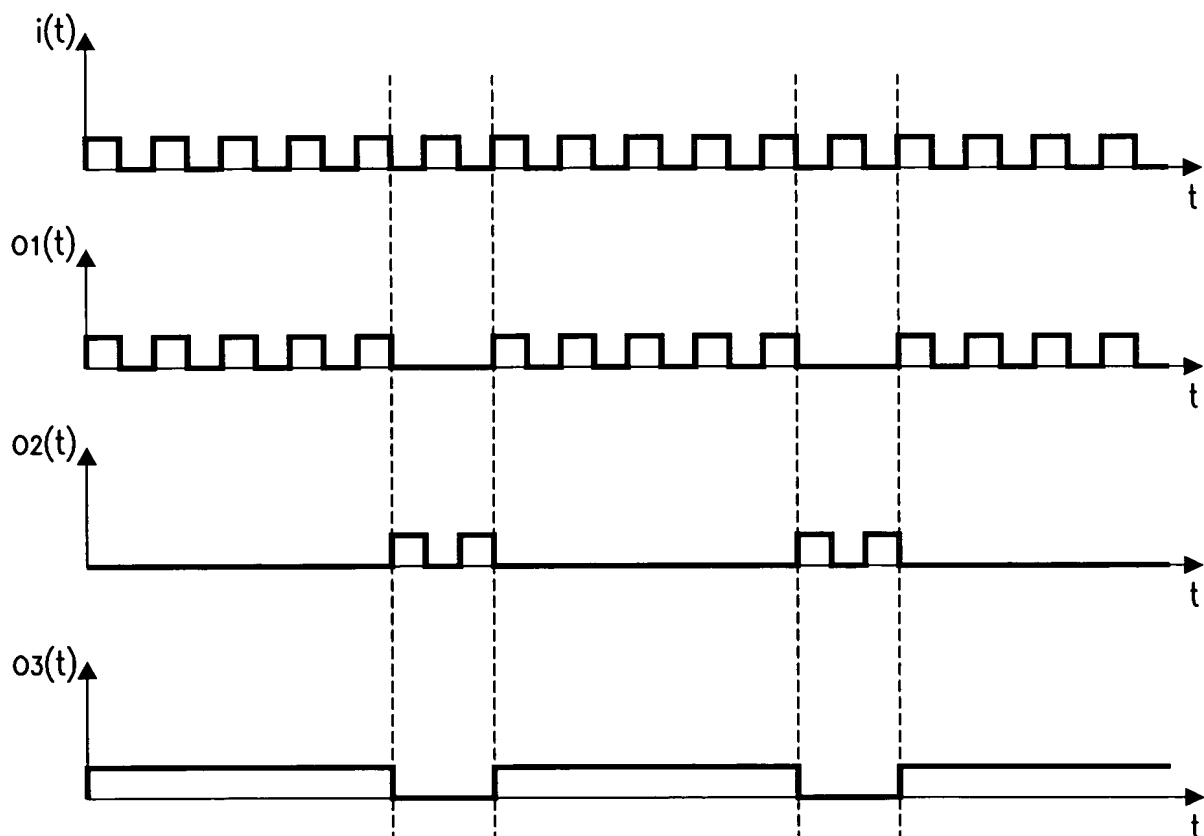


FIG. 5B

EXAMPLE TIMING DIAGRAM FOR INPUT AND OUTPUT SIGNALS OF CLOCK DIVIDER WITH DIVISION RATIO 5:2



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FIG. 6

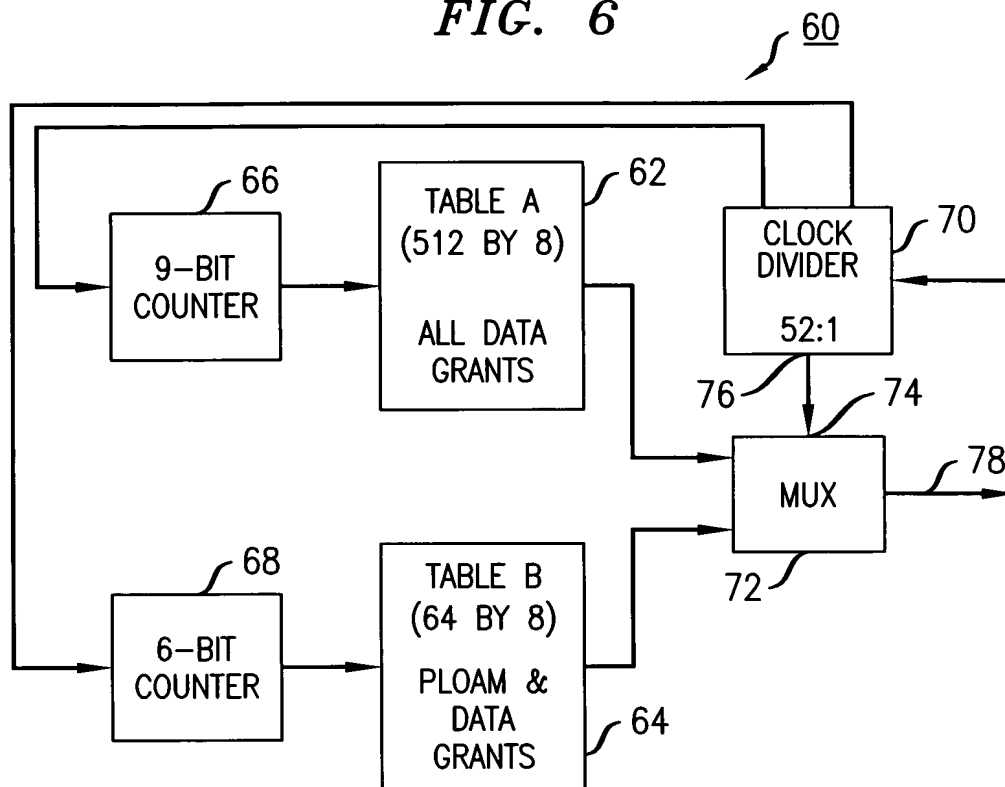


FIG. 7

